

Group Discussion Questions

- A The front end of the Sandy Bridge ISA design is unique to a CISC-on-RISC design. How is each of component of the front end designed to make the process faster and lower power?

- B How many different caches of information exist on the Sandy Bridge CPU? Why?

- C How does the PIC implement CALL and RETURN?

- D Go over how each addressing mode is implemented in the PIC architecture.

- E The Sandy Bridge CPU implements its own version of superscalar processing. Discuss the overall design and some of the specific innovations used by the CPU.