Design Process: Algebra

- Create truth table from specification
- Write the Boolean expression
- Simplify the expression (algebraic simplification)
- Draw the logic diagram
- Build the circuit
Design Process: K-map

- Create truth table from specification
- Karnaugh map (K-map)
- Write the Boolean expression
- Draw the logic diagram
- Build the circuit
Karnaugh Map

- A convenient way of representing a Boolean expression of a small number (up to 4) of variables.

- The map is an array of $2^n$ squares, representing all possible combinations of values of $n$ binary variables.