

VHDL (I)

A Full Adder

- Before introducing you to VHDL, let's build a simple circuit, so that we can use it to calculate binary adder.

A	B	SUM	Carry
0	0	0	0
0	1	1	0
1	0	1	0
1	1	0	1

- Compare the truth table with the ones of those basic gates
 - Carry is AND
 - SUM is XOR

